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**A Smart Failure Mode And Effect Analysis Package**AZ Keller, PV Fleming - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... between 1 and 10. A Risk Priority Sum (RPS) is also calculated as the sum of RPNs all contributing causes of failure. As the basis ...

[Web Search](#)**The Architecture of a High Speed Switch for Deflection Networks**G Albertengo, R Masera, G Panizzardi, GR Roch, MD ... - Electrotechnical Conference, 1991. Proceedings., 6th ..., 1991 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... adders. As the best routing is associated with the highest priority sum, a special circuit has been designed to detect it. It ...

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... for each outgoing (connected) edge: - number of lanes to use to reach this edge = this edge's priority for the current edge / **priority sum** - if number ...

[View as HTML](#) - [Web Search](#) - [eprints.vf.bwl.de](http://eprints.vf.bwl.de)**Job Shop Optimisation Using Genetic Algorithms**R Mathur - [members.lycos.co.uk](http://members.lycos.co.uk)

... Job4: 0 1 0 \* 0 0 1 Job5: 0 0 0 1 \* 1 2 Job6: 1 0 1 1 0 \* 3 Figure 8 (a) Original priority Sum Job1: \* 0 0 1 1 0 2 Job2: 1 \* 0 0 1 1 3 Job3: 1 1 \* 1 1 1 1 5 ...

[View as HTML](#) - [Web Search](#)**Evaluating the GPRS Radio Interface for Different Quality of Service Profiles**C Lindemann, A Thuemmler - Kommunikation in Verteilten Systemen, 2001 - [rul-www.cs.uni-dortmund.de](http://rul-www.cs.uni-dortmund.de)

... Call Arrival Rate Carried Data Traffic Low Priority Medium Priority High

Priority Sum 1e-05 1e-04 1e-03 1e-02 1e-01 1e+00 0,0 0,2 ...

Cited by 3 - [View as HTML](#) - [Web Search](#) - [control.auc.dk](http://control.auc.dk) - [warlock.cs.waikato.ac.nz](http://warlock.cs.waikato.ac.nz) - [wand.net.nz](http://wand.net.nz) - all 9 versions »

**DESIGN PRINCIPLES AND NEW DEVELOPMENTS IN THE AMPL MODELING LANGUAGE**R Fourer, DM Gay, BW Kernighan - [users.iems.northwestern.edu](http://users.iems.northwestern.edu)

Page 1. Chapter 1 DESIGN PRINCIPLES AND NEW DEVELOPMENTS IN THE AMPL MODELING LANGUAGE Robert Fourer Department of Industrial Engineering ...

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... It does not, however, maximize the **priority sum** of selected regularities. Priorities are only used to compare the regularities in case of an inconsistency. ...

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**[PS] From Processor Timing Specifications to Static Instruction Scheduling**EA Harcourt, J Mauney, TA Cook - SAS, 1994 - [cs.chalmers.se](http://cs.chalmers.se)

... denote the binary combinator Next to denote this process: A Next B def = A (1 : B )

Another useful operator is the **priority sum** operator, [CW91]. If in the ...

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**Performance assertion checking**SE Perl, WE Weihl - 1992 - [ics.mit.edu](http://ics.mit.edu)

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**Chip layout optimization using critical path weighting**

AE Dunlop, VD Agrawal, DN Deutsch, MF Jukl, P ... - 21st Proceedings of the Design Automation Conference on ... - portal.acm.org

... 0 to 12fo MHZ Figure 2. Priority weight assignment for signal nets on clock paths.

Figure 3. Priority weight assignment for signal nets on combinational paths. ...

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**A New Min-Cut Placement Algorithm for Timing Assurance Layout Design Meeting Net Length Constraint**

M Terai, K Takahashi, K Sato - DAC, 1990 - portal.acm.org

... Timing assurance placement and routing methods have been reported previously

[2]-[7]. Priority weight assignment for controlling wire length in automatic ...

Cited by 13 - Web Search - portal.acm.org - ieeexplore.ieee.org

**Processes with probabilities, priority and time**

C Tofts - Formal Aspects of Computing, 1994 - springerlink.com

... equivalence. Obviously this cannot be well defined for processes with unresolved priority weight transitions. Definition 2.13. We ...

Cited by 50 - Web Search - pharos.cpsc.ucalgary.ca

**Effectively prioritizing tests in development environment**

A Srivastava, J Thiagarajan - ACM SIGSOFT Software Engineering Notes, 2002 - portal.acm.org

... be covered. The algorithm starts by assigning priority weight to each test

equal to the number of impacted blocks it covers. The ...

Cited by 27 - Web Search - cs.virginia.edu - cs.arizona.edu - pag.csail.mit.edu - all 13 versions »

**Web-HIPRE-A Java applet for AHP and value tree analysis**

J Mustajoki, RP Haemaelainen - INFOR Journal, 2000 - hut.fi

... On the alternative level, AHP weights can be converted to the compatible 0–1 value scale by setting the lowest priority weight is to zero, the highest ...

Cited by 4 - View as HTML - Web Search - sal.hut.fi - hut.fi - sal.hut.fi

**Business process management: survey and methodology**

DJ Elzinga, T Horak, CY Lee, C Bruner - IEEE Transactions on Engineering Management, 1995 - ieeexplore.ieee.org

... attribute. 4) Determination of the overall priority weight of each alternative and, hence, the ranking of all alternatives. The ...

Cited by 12 - Web Search - ieeexplore.ieee.org

**Static-priority periodic scheduling on multiprocessors**

S Ramamurthy, M Moir - 21 st IEEE Real-Time Systems Symposium(RTSS 2000), 2000 - ieeexplore.ieee.org

... condition for the schedulability of preemptable, periodic, hard-real-time task sets using the very simple static-priority weight-monotonic scheduling scheme. ...

Cited by 7 - Web Search - sunlabs.com - cs.pitt.edu - sun.com - all 11 versions »

**Weighted arbitration algorithms with priorities for input-queued switches with 100% throughput**

R Schoenen, G Post, G Sander - Proceedings of the IEEE Broadband Switching Systems, 1999 - iss.rwth-aachen.de

Page 1. Weighted Arbitration Algorithms with Priorities for Input-Queued

Switches with 100% Throughput Rainer Schoenen and Guido ...

Cited by 12 - View as HTML - Web Search - schoenen-service.de - iss.rwth-aachen.de - ert.rwth-aachen.de

**Demand-driven Service Differentiation in Cluster-based Network Servers**

H Zhu, H Tang, T Yang - INFOCOM, 2001 - ieeexplore.ieee.org

... Briefly speaking, our scheme allows a site administrator to create an arbitrary

number of service quality classes and assign a **priority weight** for each class ...

Cited by 43 - Web Search - [axp1.csie.ncu.edu.tw](http://axp1.csie.ncu.edu.tw) - 24.237.160.4 - it.iitb.ac.in - all 9 versions »

### Information priority-setting for better resource allocation using analytic hierarchy process (AHP)

EWL Cheng, H Li - Information Management and Computer Security, 2001 - [ingentaconnect.com](http://ingentaconnect.com)

... adding the elements in each resulting row (to obtain ``a row sum'') and dividing  
this sum by the number of elements in the row (to obtain ``**priority weight** ...

Cited by 5 - Web Search - [ingentaconnect.com](http://ingentaconnect.com)

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"priority weight"

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Relevance scale

**1 A Lotos extension for the performance analysis of distributed systems**Marco Ajmone Marsan, Andrea Bianco, Luigi Ciminiera, Riccardo Sisto, Adriano Valenzano  
April 1994 **IEEE/ACM Transactions on Networking (TON)**, Volume 2 Issue 2**Publisher:** IEEE PressFull text available: [pdf\(1.59 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)**2 Tools & techniques track: applying machine learning to collection development:** **What's there and what's not?: focused crawling for missing documents in digital libraries**

Ziming Zhuang, Rohit Wagle, C. Lee Giles

June 2005 **Proceedings of the 5th ACM/IEEE-CS joint conference on Digital libraries****Publisher:** ACM PressFull text available: [pdf\(346.33 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Some large scale topical digital libraries, such as CiteSeer, harvest online academic documents by crawling open-access archives, university and author homepages, and authors' self-submissions. While these approaches have so far built reasonable size libraries, they can suffer from having only a portion of the documents from specific publishing venues. We propose to use alternative online resources and techniques that maximally exploit other resources to build the complete document collection of ...

**Keywords:** ACM, CiteSeer, DBLP, digital libraries, focused crawler, harvesting**3 Effectively prioritizing tests in development environment**

Amitabh Srivastava, Jay Thiagarajan

July 2002 **ACM SIGSOFT Software Engineering Notes , Proceedings of the 2002 ACM SIGSOFT international symposium on Software testing and analysis ISSTA '02**, Volume 27 Issue 4**Publisher:** ACM PressFull text available: [pdf\(381.17 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

Software testing helps ensure not only that the software under development has been implemented correctly, but also that further development does not break it. If developers introduce new defects into the software, these should be detected as early and inexpensively as possible in the development cycle. To help optimize which tests are run at what points in the design cycle, we have built *Echelon*, a test prioritization system, which prioritizes the application's given set of tests, based ...

**Keywords:** regression testing, software testing, test minimization, test prioritization, test selection

**4 Papers: A novel wide-band audio transmission scheme over the Internet with a smooth quality degradation**

Fulvio Babich, Marko Vitez

January 2000 **ACM SIGCOMM Computer Communication Review**, Volume 30 Issue 1

**Publisher:** ACM Press

Full text available:  pdf(768.81 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

Real-time delivery of multimedia information over the Internet is finding increasing interest. This paper considers wide-band audio transmission utilizing a priority scheme. The proposed scheme complies with both the new Internet Protocol Version 6 (IPv6) and the current Internet Protocol Version 4 (IPv4), providing that, in the latter case, routers are set to manage priority. A new queuing algorithm, namely Priority Weighted Fair Queuing (PWFQ), is defined and evaluated. A scalable audio encode ...

**Keywords:** Audio on Demand, Internet applications, Multimedia, Quality of Service, Technical subjects, Weighted Fair Queuing

**5 Performance evaluation of routing algorithms under various network configuration parameters**

Shin-Jer Yang

July 1997 **International Journal of Network Management**, Volume 7 Issue 4

**Publisher:** John Wiley & Sons, Inc.

Full text available:  pdf(380.88 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Routing is a relevant issue for maintaining good performance and successfully operating in a network. Many types of routing algorithms have been proposed, such as shortest-path, centralized, distributed and flow-based. This article not only defines the relationship among performance indicators and network configuration parameters, but also presents a practical method for improving routing decisions. © 1997 John Wiley & Sons, Ltd.

**6 SAIRVO: a temporal planning system**

 M. Lizotte, B. Moulin

June 1989 **Proceedings of the 2nd international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1 IEA/AIE '89**

**Publisher:** ACM Press

Full text available:  pdf(619.37 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Our approach of planification which derives from both expert systems and planning systems design techniques (MOU 86)(MOU 88), is based on several principles dealing with time management and the planner's universe modelling, the representation of its motivations and behavioural knowledge, the dynamic construction of its planning knowledge, as well as the use of metaplanning techniques to coordinate the planners reasoning activities. We will present a brief outline of research works related t ...

**7 Virtual 3D camera composition from frame constraints**

 William Bares, Scott McDermott, Christina Boudreaux, Somying Thainimit

October 2000 **Proceedings of the eighth ACM international conference on Multimedia**

**Publisher:** ACM Press

Full text available:  pdf(1.75 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We have designed a graphical interface that enables 3D visual artists or developers of interactive 3D virtual environments to efficiently define sophisticated camera compositions by creating storyboard frames, indicating how a desired shot should appear. These storyboard frames are then automatically encoded into an extensive set of virtual camera constraints that capture the key visual composition elements of the storyboard frame. Visual composition elements include the size and position of ...

**Keywords:** constraints, user interfaces, virtual 3D cameras

**8 Chip layout optimization using critical path weighting**

A. E. Dunlop, V. D. Agrawal, D. N. Deutsch, M. F. Jukl, P. Kozak, M. Wiesel  
June 1984 **Proceedings of the 21st conference on Design automation**

**Publisher:** IEEE Press

Full text available:  pdf(276.22 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A chip layout procedure for optimizing the performance of critical timing paths in a synchronous digital circuit is presented. The procedure uses the path analysis data produced by a static timing analysis program to generate weights for critical nets on clock and data paths. These weights are then used to bias automatic placement and routing in the layout program. This approach is shown to bring the performance of the chip significantly closer to that of an ideal layout which is assumed to ...

**9 Chip layout optimization using critical path weighting**

 A. E. Dunlop, V. D. Agrawal, D. N. Deutsch, M. F. Jukl, P. Kazak  
June 1988 **Papers on Twenty-five years of electronic design automation**

**Publisher:** ACM Press

Full text available:  pdf(346.05 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

**10 Object model resurrection — an object oriented maintenance activity**

 Gokul V. Subramaniam  
June 2000 **Proceedings of the 22nd international conference on Software engineering**

**Publisher:** ACM Press

Full text available:  pdf(149.48 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper addresses the problem of reengineering object-oriented systems that have incurred increased maintenance cost due to long development time-span and project lifecycle. When an Incremental Approach is used to develop an object-oriented system, there is a risk that the class design and the overall object model will deteriorate in quality with each increment. A recent research work suggested a process activity (Class Deterioration Detection and Resurrection - CDDR pro ...

**Keywords:** class reengineering, object oriented reengineering/maintenance/metrics, process models

**11 Quality function deployment usage in software development**

 Stephen Haag, M. K. Raja, L. L. Schkade  
January 1996 **Communications of the ACM**, Volume 39 Issue 1

**Publisher:** ACM Press

Full text available:  pdf(259.56 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#), [review](#)

**12 A new min-cut placement algorithm for timing assurance layout meeting net length constraint**

 Masayuki Terai, Kazuhiro Takahashi, Koji Sato  
January 1991 **Proceedings of the 27th ACM/IEEE conference on Design automation**

**Publisher:** ACM Press

Full text available:  pdf(776.15 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper presents a new min-cut placement algorithm for timing assurance layout design. When critical nets are given net length constraints, the proposed algorithm can place cells so that the constraints may be met. This algorithm is built into the layout system for gate arrays called GALOP [1] and has been successfully applied to clock skew control of an ECL 12K-gate array.

**13 Technical correspondence: When and what to compile/optimize in a virtual machine?** 

 K. V. Seshu Kumar

March 2004 **ACM SIGPLAN Notices**, Volume 39 Issue 3

**Publisher:** ACM Press

Full text available:  pdf(731.84 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

To speed up the computation of some of the object languages, virtual machines use dynamic compilation. But due to compilation taking place during user application runtime, one has to choose which methods to compile, so that the compile time has little impact on the total runtime of the actual application. In order to achieve this, we have to use an estimate to determine the process of compilation. Current virtual machines use run time information such as number of calls made to this method, size ...

**Keywords:** online profiling, relative estimation for hotness detection, selective compilation, tier compilation

**14 Tools: Virtual routers: a tool for networking research and education** 

 Florian Baumgartner, Torsten Braun, Eveline Kurt, Attila Weyland

July 2003 **ACM SIGCOMM Computer Communication Review**, Volume 33 Issue 3

**Publisher:** ACM Press

Full text available:  pdf(284.61 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Virtual routers are software entities, i.e. user space processes, emulating IP routers on one or several (Linux) computers. Virtual routers can be used for both networking research and education. In contrast to simulation, virtual routers process packets in real-time and the virtual router code is similar to code in real routers. In the case of research, larger network test-beds can be built using a relatively small number of computers. New functionalities such as new queuing mechanisms are supp ...

**Keywords:** distance learning, network emulation, networking, performance evaluation

**15 Planning in MAS: Continual coordination through shared activities** 

 Bradley J. Clement, Anthony C. Barrett

July 2003 **Proceedings of the second international joint conference on Autonomous agents and multiagent systems**

**Publisher:** ACM Press

Full text available:  pdf(200.06 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Interacting agents that interleave planning and execution must reach consensus on their commitments to each other. In domains where agents have varying degrees of interaction and different constraints on communication and computation, agents will require different coordination protocols in order to efficiently reach consensus in real time. We briefly describe a largely unexplored class of real-time, distributed planning problems (inspired by interacting spacecraft missions), new challenges they ...

**Keywords:** communication protocols, coordination, multiagent planning

**16 Survey of design goals for operating systems** 

 David H. Abernathy, John S. Mancino, Charls R. Pearson, Dona C. Swiger

January 1974 **ACM SIGOPS Operating Systems Review**, Volume 8 Issue 1

**Publisher:** ACM Press

Full text available:  pdf(502.66 KB) Additional Information: [full citation](#), [abstract](#)

The paper reports the results of a literature search on the subject "design goals for operating systems." In addition, design goals of specific operating systems are reviewed and a general set of operating system design goals is developed. Special-purpose design goals and conflicts among design goals are also discussed.

**17 Prediction of interconnect delay in logic synthesis**

IC designers are now increasingly concerned about the delay due to interconnection wires. In the past, these effects have been largely ignored during logic design—primarily due to their negligible contributions and also because of the difficulty of predicting the wiring resulting from the subsequent layout stage. In this paper, an estimation model is proposed to predict the average wire length for each net in a given gate-level netlist and a particular layout tool.

**Keywords:** IC design, average wire length, delays, estimation model, estimation theory, gate-level netlist, integrated circuit interconnections, integrated circuit layout, integrated circuit modelling, integrated logic circuits, interconnect delay prediction, interconnection wires, layout tool, logic design, logic synthesis, piecewise-linear techniques

**18 Self-assessment procedure XX**

 J. Rosenberg, A. L. Ananda, B. Srinivasan

February 1990 **Communications of the ACM**, Volume 33 Issue 2

**Publisher:** ACM Press

Full text available:  pdf(1.06 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A self-assessment procedure on operating systems

**19 Prioritized resource allocation for stressed networks**

Cory C. Beard, Victor S. Frost

October 2001 **IEEE/ACM Transactions on Networking (TON)**, Volume 9 Issue 5

**Publisher:** IEEE Press

Full text available:  pdf(253.05 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Overloads that occur during times of network stress result in blocked access to all users, independent of importance. These overloads can occur because of degraded resource availability or abnormally high demand. Public broadband networks must dynamically recognize some multimedia connections as having greater importance than others and allocate resources accordingly. A new approach to connection admission control is proposed that uses an upper limit policy to optimize the admission of connectio ...

**Keywords:** Computer network performance, resource management

**20 A quick safari through the reconfiguration jungle**

 Patrick Schaumont, Ingrid Verbauwheide, Kurt Keutzer, Majid Sarrafzadeh

June 2001 **Proceedings of the 38th conference on Design automation**

**Publisher:** ACM Press

Full text available:  pdf(268.82 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Cost effective systems use specialization to optimize factors such as power consumption, processing throughput, flexibility or combinations thereof. Reconfigurable systems obtain this specialization at run-time. System reconfigurable has a vertical, a horizontal and a time dimension. We organize this design space as the reconfiguration hierarchy, and discuss the design methods that deal with it. Finally, we survey existing commercial platforms that support reconfiguration and situate them i ...

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Relevance scale      **1 A smalltalk window system based on constraints**

Danny Epstein, Wilf R. LaLonde

January 1988 **ACM SIGPLAN Notices , Conference proceedings on Object-oriented programming systems, languages and applications OOPSLA '88**, Volume 23 Issue 11**Publisher:** ACM PressFull text available: [pdf\(1.28 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We describe the design of a constraint-based window system for Smalltalk. This window system uses constraints to specify attributes of windows and relationships between them. Three classes of constraints are supported, one of which is implicit and not available for general use. The system extends the current Smalltalk system, providing support for both fixed-size and fixed-scale windows. It also provides the capability to dynamically reorganize the layout of a window. A goal of the design i ...

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